## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|---|---|---|---|
| Applicants: | Alex MATUSEVICH | Conf: | 1477 |
| Application No.: | 09/156,761 | Group: | 2663 |
| Filed: | September 18, 1998 | Examiner: | Jasper C. KWOH |

For:         TDMA COMMUNICATION SYSTEM AND METHOD INCLUDING DISCONTINUOUS MODULATION FOR REDUCING ADJACENT AND CO-CHANNEL INTERFERENCE

## DRAWING CORRECTION APPROVAL REQUEST

**RECEIVED**

Assistant Commissioner for Patents
Washington, D.C. 20231

May 1, 2002

**MAY 0 6 2002**

Dear Sir:

**Technology Center 2600**

Applicant respectfully requests the Examiner's approval of the following drawing correction indicated in red ink on the attached sheets. The following is the correction.

Figures 1-3 have been designated by a legend of --Prior Art--.

No new matter has been entered via the drawing correction. Corrected formal drawings will be prepared in accordance with the Notice of Draftperson's Patent Drawing Review Form PTO-948 and will be filed upon approval by the Examiner and subsequent indication of allowance of the present application.

If there are any outstanding matters remaining in this application, the Examiner is invited to contact Timothy R. Wyckoff, Reg. No. 46,175, at the number listed below in the Washington, D.C. area in order to discuss these matters.

If necessary, the Commissioner is hereby authorized in this, concurrent, and further replies, to charge payment or credit any overpayment to Deposit Account No. **08-0750** for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKEY & PIERCE, P.L.C

By: _____

Gary D. Yacura, Reg. No. 35,416

GDY/TRW:tljw

P.O. Box 8910
Reston, Virginia 20195
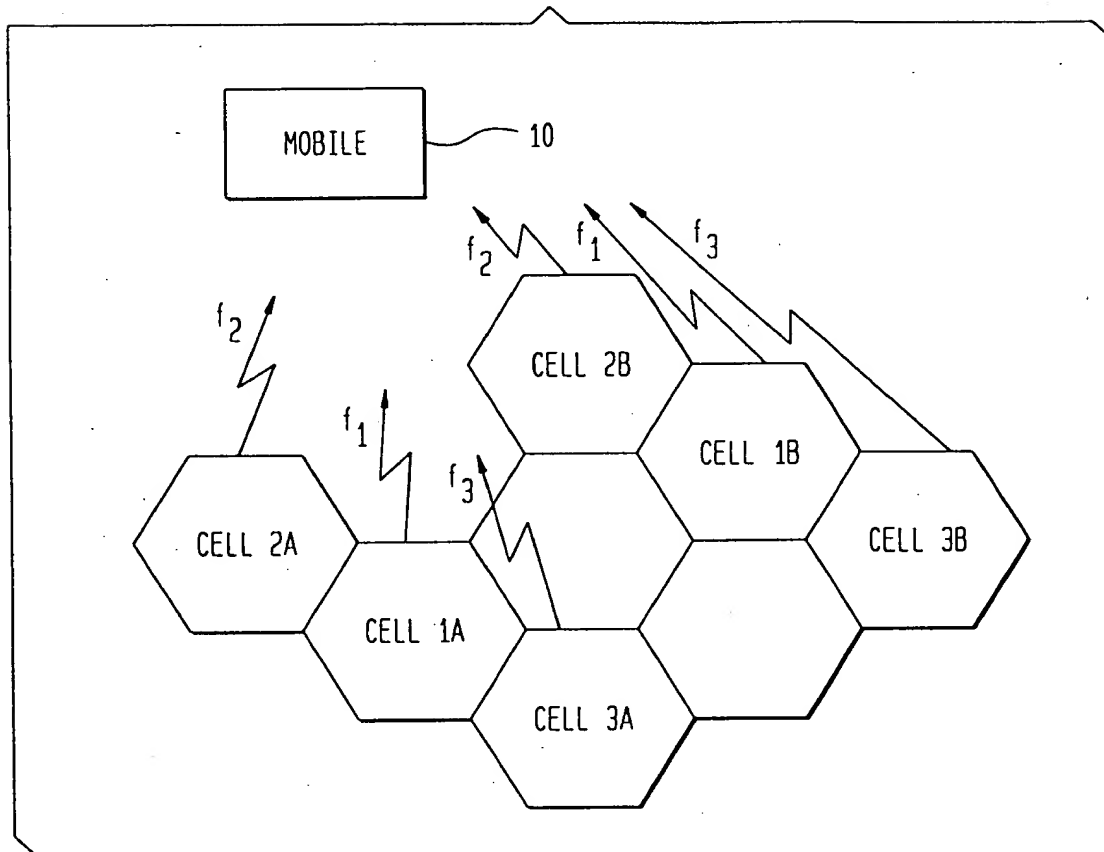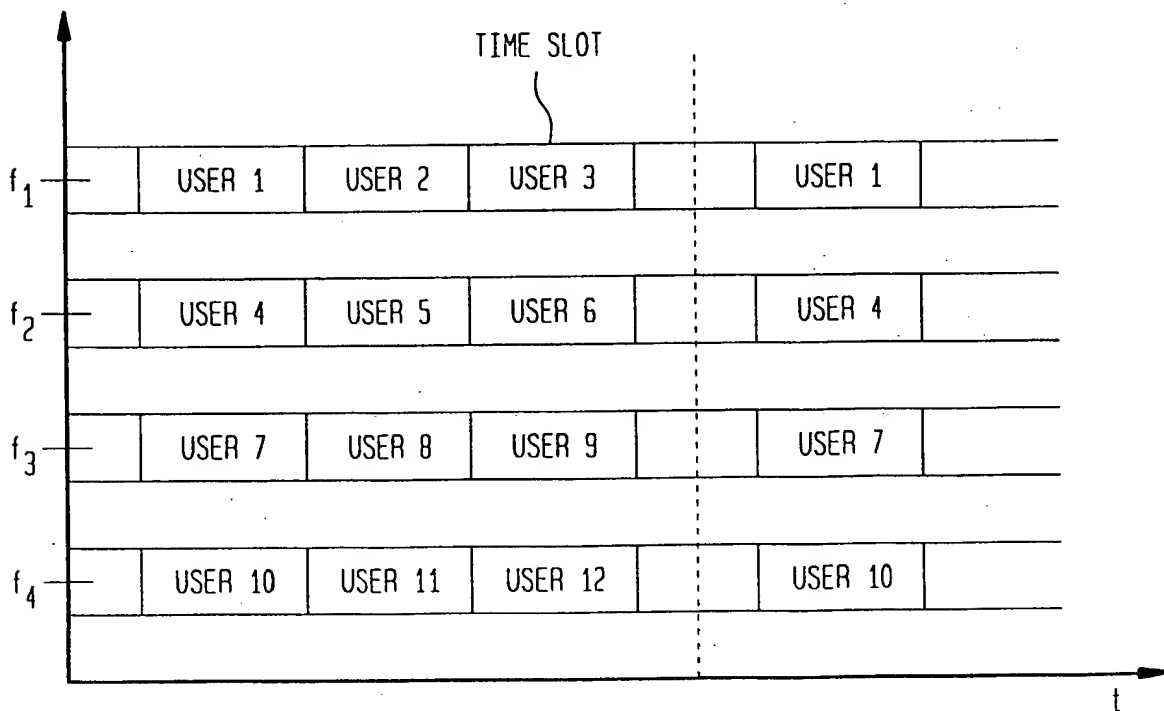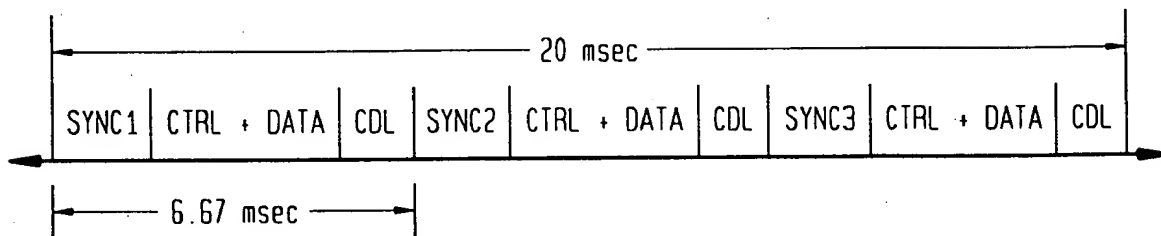(703) 390-3030

Attachment: As stated above

FIG. 1 (Prior Art)



FIG. 2 (Prior Art)

approved
9/1C
7/5/02

FIG. 3 (Prior Art)

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| SYNC1 | CTRL + DATA | CDL | SYNC2 | CTRL + DATA | CDL | SYNC3 | CTRL + DATA | CDL |

20 msec

6.67 msec

approved
7/5/12

FIG. 5



TIME SLOT

| | | | |
|---|---|---|---|
| A | I | I | |

$f_1$

| | | | |
|---|---|---|---|
| I | A | I | |

$f_2$

30 KHz

| | | | |
|---|---|---|---|
| I | I | A | |

$f_3$

| | | | |
|---|---|---|---|
| A | A | I | |

$f_4$

t